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We have shown the implementation of the various buffers, the data flow and the control flow for a pipelined implementation of the MIPS architecture. Web Links / Supporting Materials Computer Organization and Design – The Hardware / Software Interface, David A. Patterson and John L. Hennessy, 4th.Edition, Morgan Kaufmann, Elsevier, 2009.

Pipelining - MIPS Implementation - Computer Architecture

In computer engineering, computer architecture is a set of rules and methods that describe the functionality, organization, and implementation of computer systems. Some definitions of architecture define it as describing the capabilities and programming model of a computer but not a particular implementation. In other definitions computer architecture involves instruction set architecture design, microarchitecture design, logic design, and implementation.

Computer architecture - Wikipedia

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Building Data Path and Control Implementation Scheme

Offered by Princeton University. In this course, you will learn to design the computer architecture of complex modern microprocessors. All the features of this course are available for free. It does not offer a certificate upon completion.

Computer Architecture | Coursera

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Computer Architecture can be considered as an agreement between H/W and S/W people. That is,

what all feature is being provided by the H/W programmer is depicted here. for e.g. "add a,b,c " is an instruction which is capable of adding two numbers. NOTE: Computer Architecture boundary is not only confined to ISA, but it move beyond. In short, what I can tell you is, as a OS coder or a Compiler coder whatever instruction you use to talk to H/W can be considered as a part of Computer Architecture.

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Computer science - Wikipedia

In computer fields, computer architecture is a set of rules and ways that explain the functionality, organization, and implementation of computer systems. The design, arrangement , construction or organization of the different parts of a computer system is known as Computer Architecture.