
Computer Organization And Design 5th Edition

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**Computer
Organization and
Design MIPS Edition**

**The
Hardware/Software
Interface** *Morgan
Kaufmann*

Computer Organization and Design: The Hardware/Software Interface, Sixth Edition, the leading, award-winning textbook from Patterson and Hennessy used by more than 40,000

students per year, continues to present the most comprehensive and readable introduction to this core computer science topic. Improvements to this new release include new sections in each chapter on Domain Specific Architectures (DSA) and updates on all real-world examples that keep it fresh and relevant for a new generation of students. Covers parallelism in-depth, with examples and content highlighting parallel hardware and software topics Includes new sections in each chapter on Domain Specific Architectures (DSA) Discusses and highlights the "Eight Great Ideas" of computer architecture, including Performance via Parallelism,

Performance via Pipelining,
Performance via Prediction, Design for Moore's Law, Hierarchy of Memories, Abstraction to Simplify Design, Make the Common Case Fast and Dependability via Redundancy

Computer Organization and Design

The Hardware/software Interface *Morgan Kaufmann*

The performance of software systems is dramatically affected by how well software designers understand the basic hardware technologies at work in a system. Similarly, hardware designers must understand the far-reaching effects their design decisions

have on software applications. For readers in either category, this classic introduction to the field provides a look deep into the computer. It demonstrates the relationships between the software and hardware and focuses on the foundational concepts that are the basis for current computer design.

Computer Organization and Design

The Hardware/Software Interface, Third Edition *Elsevier*

This best selling text on computer organization has been thoroughly updated to reflect the newest technologies. Examples highlight the latest processor designs,

benchmarking standards, languages and tools. As with previous editions, a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system. The book presents an entire MIPS instruction set—instruction by instruction—the fundamentals of assembly language, computer arithmetic, pipelining, memory hierarchies and I/O. A new aspect of the third edition is the explicit connection between program performance and CPU performance. The authors show how hardware and software components--such as the specific algorithm, programming language, compiler, ISA and processor implementation--

impact program performance.

Throughout the book a new feature focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system. The book digs deeper into the hardware/software interface, presenting a complete view of the function of the programming language and compiler--crucial for understanding computer organization. A CD provides a toolkit of simulators and compilers along with tutorials for using them. For instructor resources click on the grey "companion site" button found on the right side of this page. This new edition represents a major revision. New to this

edition: * Entire Text has been updated to reflect new technology * 70% new exercises. * Includes a CD loaded with software, projects and exercises to support courses using a number of tools * A new interior design presents defined terms in the margin for quick reference * A new feature, "Understanding Program Performance" focuses on performance from the programmer's perspective * Two sets of exercises and solutions, "For More Practice" and "In More Depth," are included on the CD * "Check Yourself" questions help students check their understanding of major concepts * "Computers In the Real World" feature illustrates the diversity

of uses for information technology *More detail below...

Computer Organization and Design RISC-V Edition

The Hardware Software Interface

Morgan Kaufmann

The new RISC-V Edition of Computer Organization and Design features the RISC-V open source instruction set architecture, the first open source architecture designed to be used in modern computing environments such as cloud computing, mobile devices, and other embedded systems. With the post-PC era now upon us, Computer Organization and Design moves forward to explore this

generational change with examples, exercises, and material highlighting the emergence of mobile computing and the Cloud. Updated content featuring tablet computers, Cloud infrastructure, and the x86 (cloud computing) and ARM (mobile computing devices) architectures is included. An online companion Web site provides advanced content for further study, appendices, glossary, references, and recommended reading. Features RISC-V, the first such architecture designed to be used in modern computing environments, such as cloud computing, mobile devices, and other embedded systems Includes relevant examples,

exercises, and material highlighting the emergence of mobile computing and the cloud

Computer Organization and Design

The Hardware/Software Interface *Elsevier*

"Presents the fundamentals of hardware technologies, assembly language, computer arithmetic, pipelining, memory hierarchies and I/O"--

Computer Organization, Design, and Architecture, Fifth Edition *CRC Press*

Suitable for a one- or two-semester undergraduate or beginning graduate course in computer science and computer

engineering, Computer Organization, Design, and Architecture, Fifth Edition presents the operating principles, capabilities, and limitations of digital computers to enable the development of complex yet efficient systems. With 11 new sections and four revised sections, this edition takes students through a solid, up-to-date exploration of single- and multiple-processor systems, embedded architectures, and performance evaluation. See What's New in the Fifth Edition Expanded coverage of embedded systems, mobile processors, and cloud computing Material for the "Architecture and Organization" part of the 2013 IEEE/ACM Draft Curricula for

Computer Science and Engineering Updated commercial machine architecture examples The backbone of the book is a description of the complete design of a simple but complete hypothetical computer. The author then details the architectural features of contemporary computer systems (selected from Intel, MIPS, ARM, Motorola, Cray and various microcontrollers, etc.) as enhancements to the structure of the simple computer. He also introduces performance enhancements and advanced architectures including networks, distributed systems, GRIDs, and cloud computing. Computer organization deals with providing just enough details on the

operation of the computer system for sophisticated users and programmers. Often, books on digital systems' architecture fall into four categories: logic design, computer organization, hardware design, and system architecture. This book captures the important attributes of these four categories to present a comprehensive text that includes pertinent hardware, software, and system aspects.

The Essentials of Computer

Organization and Architecture *Jones & Bartlett Learning*

Updated and revised, The Essentials of Computer Organization and Architecture, Third Edition is a comprehensive resource that

addresses all of the necessary organization and architecture topics, yet is appropriate for the one-term course.

Beginning Software Engineering *John Wiley & Sons*

A complete introduction to building robust and reliable software. Beginning Software Engineering demystifies the software engineering methodologies and techniques that professional developers use to design and build robust, efficient, and consistently reliable software. Free of jargon and assuming no previous programming, development, or management experience, this accessible guide

explains important concepts and techniques that can be applied to any programming language. Each chapter ends with exercises that let you test your understanding and help you elaborate on the chapter's main concepts. Everything you need to understand waterfall, Sashimi, agile, RAD, Scrum, Kanban, Extreme Programming, and many other development models is inside! Describes in plain English what software engineering is. Explains the roles and responsibilities of team members working on a software engineering project. Outlines key phases that any software engineering effort must handle to produce applications that are powerful and

dependable Details the most popular software development methodologies and explains the different ways they handle critical development tasks Incorporates exercises that expand upon each chapter's main ideas Includes an extensive glossary of software engineering terms

The Architecture of Computer Hardware, Systems Software, and Networking

An Information Technology

Approach *John Wiley & Sons*

The Architecture of Computer Hardware, Systems Software and Networking is designed help students majoring in information technology (IT) and information systems

(IS) understand the structure and operation of computers and computer-based devices. Requiring only basic computer skills, this accessible textbook introduces the basic principles of system architecture and explores current technological practices and trends using clear, easy-to-understand language. Throughout the text, numerous relatable examples, subject-specific illustrations, and in-depth case studies reinforce key learning points and show students how important concepts are applied in the real world. This fully-updated sixth edition features a wealth of new and revised content that reflects today's technological landscape. Organized

into five parts, the book first explains the role of the computer in information systems and provides an overview of its components.

Subsequent sections discuss the representation of data in the computer, hardware architecture and operational concepts, the basics of computer networking, system software and operating systems, and various interconnected systems and components. Students are introduced to the material using ideas already familiar to them, allowing them to gradually build upon what they have learned without being overwhelmed and develop a deeper knowledge of computer architecture.

Computer Architecture

A Quantitative Approach *Elsevier*

The computing world today is in the middle of a revolution: mobile clients and cloud computing have emerged as the dominant paradigms driving programming and hardware innovation today. The Fifth Edition of *Computer Architecture* focuses on this dramatic shift, exploring the ways in which software and technology in the cloud are accessed by cell phones, tablets, laptops, and other mobile computing devices. Each chapter includes two real-world examples, one mobile and one datacenter, to illustrate this revolutionary change.

Updated to cover the mobile computing revolution Emphasizes the two most important topics in architecture today: memory hierarchy and parallelism in all its forms. Develops common themes throughout each chapter: power, performance, cost, dependability, protection, programming models, and emerging trends ("What's Next") Includes three review appendices in the printed text. Additional reference appendices are available online. Includes updated Case Studies and completely new exercises.

**Computer
Organization,
Design, and
Architecture, Fifth
Edition** *CRC Press*

Suitable for a one- or two-semester undergraduate or beginning graduate course in computer science and computer engineering, Computer Organization, Design, and Architecture, Fifth Edition presents the operating principles, capabilities, and limitations of digital computers to enable the development of complex yet efficient systems. With 11 new sections and four revised sections, this edition takes students through a solid, up-to-date exploration of single- and multiple-processor systems, embedded architectures, and performance evaluation. See What's New in the Fifth Edition Expanded coverage of embedded systems, mobile processors, and

cloud computing Material for the "Architecture and Organization" part of the 2013 IEEE/ACM Draft Curricula for Computer Science and Engineering Updated commercial machine architecture examples The backbone of the book is a description of the complete design of a simple but complete hypothetical computer. The author then details the architectural features of contemporary computer systems (selected from Intel, MIPS, ARM, Motorola, Cray and various microcontrollers, etc.) as enhancements to the structure of the simple computer. He also introduces performance enhancements and advanced architectures including networks,

distributed systems, GRIDs, and cloud computing. Computer organization deals with providing just enough details on the operation of the computer system for sophisticated users and programmers. Often, books on digital systems' architecture fall into four categories: logic design, computer organization, hardware design, and system architecture. This book captures the important attributes of these four categories to present a comprehensive text that includes pertinent hardware, software, and system aspects.

Computer Systems

Jones & Bartlett Learning

Completely revised and updated, Computer Systems, Fourth

Edition offers a clear, detailed, step-by-step introduction to the central concepts in computer organization, assembly language, and computer architecture. Important Notice: The digital edition of this book is missing some of the images or content found in the physical edition.

Computer Architecture

A Quantitative Approach *Morgan Kaufmann*

Computer Architecture: A Quantitative Approach, Sixth Edition has been considered essential reading by instructors, students and practitioners of computer design for over 20 years. The sixth edition of this classic textbook from

Hennessy and Patterson, winners of the 2017 ACM A.M. Turing Award recognizing contributions of lasting and major technical importance to the computing field, is fully revised with the latest developments in processor and system architecture. The text now features examples from the RISC-V (RISC Five) instruction set architecture, a modern RISC instruction set developed and designed to be a free and openly adoptable standard. It also includes a new chapter on domain-specific architectures and an updated chapter on warehouse-scale computing that features the first public information on Google's newest WSC. True to its original

mission of demystifying computer architecture, this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening, while always keeping an emphasis on good engineering design. Winner of a 2019 Textbook Excellence Award (Texty) from the Textbook and Academic Authors Association Includes a new chapter on domain-specific architectures, explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore's Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse-scale computing, with the first public information on the newest Google WSC Offers updates to other chapters including new material dealing with the use of stacked DRAM; data on the performance of new NVIDIA Pascal GPU vs. new AVX-512 Intel Skylake CPU; and extensive additions to content covering multicore architecture and organization Includes "Putting It All Together" sections near the end of every chapter, providing real-world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available

online Includes updated and improved case studies and exercises ACM named John L. Hennessy and David A. Patterson, recipients of the 2017 ACM A.M. Turing Award for pioneering a systematic, quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry

Parallel Computer Organization and Design *Cambridge University Press*

Teaching fundamental design concepts and the challenges of emerging technology, this textbook prepares students for a career designing the computer systems of the future. In-depth coverage of

complexity, power, reliability and performance, coupled with treatment of parallelism at all levels, including ILP and TLP, provides the state-of-the-art training that students need. The whole gamut of parallel architecture design options is explained, from core microarchitecture to chip multiprocessors to large-scale multiprocessor systems. All the chapters are self-contained, yet concise enough that the material can be taught in a single semester, making it perfect for use in senior undergraduate and graduate computer architecture courses. The book is also teeming with practical examples to aid the learning process,

showing concrete applications of definitions. With simple models and codes used throughout, all material is made open to a broad range of computer engineering/science students with only a basic knowledge of hardware and software.

Computer Organization and Design

The Hardware Software Interface: ARM Edition *Morgan Kaufmann Publishers*

This book presents the fundamentals of hardware technologies, assembly language, computer arithmetic, pipelining, memory hierarchies and I/O. This edition is updated for mobile computing and the cloud!

Digital Design and Computer Architecture, RISC-V Edition *Morgan Kaufmann*

The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books, this RISC-V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor.

Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of a processor. By the end of this book, readers will be able to build their own RISC-V

microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing a RISC-V processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and

motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor. Gives students a full understanding of the RISC-V instruction set architecture, enabling them to build a RISC-V processor and program the RISC-V processor in hardware simulation, software simulation, and in hardware. Includes both SystemVerilog and VHDL designs of fundamental building

blocks as well as of single-cycle, multicycle, and pipelined versions of the RISC-V architecture. Features a companion website with a bonus chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises. See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems.

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STRUCTURED COMPUTER ORGANIZATION

Computer Organization *New York ; Toronto : McGraw-Hill*

Fundamentals of Digital Logic and Microcomputer Design *John Wiley & Sons*

Fundamentals of Digital Logic and Microcomputer Design, has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition, the

author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered, such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor-based system design. Numerous examples are provided throughout the text. Coverage includes: Digital circuits at the gate and flip-flop levels Analysis and design of combinational and sequential circuits Microcomputer organization, architecture, and programming concepts Design of computer instruction sets, CPU, memory, and I/O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor's manual, available upon request Additionally, the accompanying CD-ROM, contains step-by-step procedures for installing and using Altera Quartus II software, MASM 6.11 (8086), and 68asm (68000), provides valuable simulation results via screen shots. Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems.

Computer Systems

Digital Design, Fundamentals of Computer Architecture and Assembly Language

Springer

This textbook covers digital design, fundamentals of computer architecture, and assembly language. The book starts by introducing basic number systems, character coding, basic knowledge in digital design, and components of a computer. The book goes on to discuss information representation in computing; Boolean algebra and logic gates; sequential logic; input/output; and CPU performance. The author also covers ARM architecture, ARM instructions and ARM

assembly language which is used in a variety of devices such as cell phones, digital TV, automobiles, routers, and switches. The book contains a set of laboratory experiments related to digital design using Logisim software; in addition, each chapter features objectives, summaries, key terms, review questions and problems. The book is targeted to students majoring Computer Science, Information System and IT and follows the ACM/IEEE 2013 guidelines. • Comprehensive textbook covering digital design, computer architecture, and ARM architecture and assembly • Covers basic number system and coding, basic knowledge in digital design, and

components of a computer • Features laboratory exercises in addition to objectives, summaries, key terms, review questions, and problems in each chapter

ARM Assembly Language

Fundamentals and Techniques, Second Edition *CRC Press*

Delivering a solid introduction to assembly language and embedded systems, *ARM Assembly Language: Fundamentals and Techniques, Second Edition* continues to support the popular ARM7TDMI, but also addresses the latest architectures from ARM, including Cortex™-A, Cortex-R, and Cortex-M processors—all of

which have slightly different instruction sets, programmer's models, and exception handling. Featuring three brand-new chapters, a new appendix, and expanded coverage of the ARM7™, this edition: Discusses IEEE 754 floating-point arithmetic and explains how to program with the IEEE standard notation Contains step-by-step directions for the use of Keil™ MDK-ARM and Texas Instruments (TI) Code Composer Studio™ Provides a resource to be used alongside a variety of hardware evaluation modules, such as TI's Tiva Launchpad, STMicroelectronics' iNemo and Discovery, and NXP Semiconductors' Xplorer boards Written

by experienced ARM processor designers, **ARM Assembly Language: Fundamentals and Techniques, Second Edition** covers the topics essential to writing meaningful assembly programs, making it an ideal textbook and professional reference.

Digital Design and Computer Architecture

ARM Edition *Morgan Kaufmann*

Digital Design and Computer Architecture: ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style

with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design.

The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues and C programming as well

as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

Business Data Communications

Prentice Hall

Business Data Communications, 6/e, is ideal for use in Business Data Communications, Data Communications, and introductory Networking for Business courses. Business Data Communications, 6/e, covers the fundamentals of data communications, networking, distributed applications, and network management and security. Stallings presents these concepts in a way that relates specifically to the business environment and the

concerns of business management and staff, structuring his text around requirements, ingredients, and applications. While making liberal use of real-world case studies and charts and graphs to provide a business perspective, the book also provides the student with a solid grasp of the technical foundation of business data communications. Throughout the text, references to the interactive, online animations supply a powerful tool in understanding complex protocol mechanisms. The Sixth Edition maintains Stallings' superlative support for either a research projects or modeling projects component in the course. The diverse set of projects and student exercises

enables the instructor to use the book as a component in a rich and varied learning experience and to tailor a course plan to meet the specific needs of the instructor and students.

Modern Computer Architecture and Organization

Learn x86, ARM, and RISC-V architectures and the design of smartphones, PCs, and cloud servers

Packt Publishing Ltd

A no-nonsense, practical guide to current and future processor and computer architectures, enabling you to design computer systems and develop better software applications across a variety of domains

Key Features

Understand digital circuitry with the help of transistors, logic gates, and sequential logic

Examine the architecture and instruction sets of x86, x64, ARM, and RISC-V processors

Explore the architecture of modern devices such as the iPhone X and high-performance gaming PCs

Book Description

Are you a software developer, systems designer, or computer architecture student looking for a methodical introduction to digital device architectures but overwhelmed by their complexity? This book will help you to learn how modern computer systems work, from the lowest level of transistor switching to the macro view of collaborating multiprocessor servers.

You'll gain unique insights into the internal behavior of processors that execute the code developed in high-level languages and enable you to design more efficient and scalable software systems. The book will teach you the fundamentals of computer systems including transistors, logic gates, sequential logic, and instruction operations. You will learn details of modern processor architectures and instruction sets including x86, x64, ARM, and RISC-V. You will see how to implement a RISC-V processor in a low-cost FPGA board and how to write a quantum computing program and run it on an actual quantum computer. By the end of this book, you will have a thorough understanding of modern processor and computer architectures and the future directions these architectures are likely to take. What you will learn Get to grips with transistor technology and digital circuit principles Discover the functional elements of computer processors Understand pipelining and superscalar execution Work with floating-point data formats Understand the purpose and operation of the supervisor mode Implement a complete RISC-V processor in a low-cost FPGA Explore the techniques used in virtual machine implementation Write a quantum computing program and run it on a quantum computer Who this book is for

This book is for software developers, computer engineering students, system designers, reverse engineers, and anyone looking to understand the architecture and design principles underlying modern computer systems from tiny embedded devices to warehouse-size cloud server farms. A general understanding of computer processors is helpful but not required.

**Computer
Organisation &
Architecture** McGraw-
Hill Education

This textbook provides a perfect amalgam of the basics of computer architecture, intricacies of modern assembly languages and advanced concepts

such as multiprocessor memory systems and I/O technologies. It shows the design of a processor from first principles including its instruction set, assembly-language specification, functional units, microprogrammed implementation and 5-stage pipeline. Computer Organisation and Architecture can serve as a textbook in both basic as well as advanced courses on computer architecture, systems programming, and microprocessor design. Additionally, it can also serve as a reference book for courses on digital electronics and communication. Salient Features: ? Balanced presentation of theoretical, qualitative and quantitative aspects of computer

architecture ?
 Extensive coverage of
 the ARM and x86
 assembly languages ?
 Extensive software
 support: Instruction set
 emulators, assembler,
 Logisim and VHDL
 design of the
 SimpleRisc processor

**Computer
 Organization 5th
 Edition**

**Parallel Computer
 Organization and
 Design** *Cambridge
 University Press*

A design-oriented text
 for advanced computer
 architecture courses,
 covering parallelism,
 complexity, power,
 reliability and
 performance.

**SPA Design and
 Architecture**

**Understanding
 single-page web**

applications *Simon
 and Schuster*

Summary SPA Design
 and Architecture
 teaches you the design
 and development skills
 you need to create
 SPAs. Includes an
 overview of MV*
 frameworks, unit
 testing, routing, layout
 management, data
 access, pub/sub, and
 client-side task
 automation. This book
 is full of easy-to-follow
 examples you can
 apply to the library or
 framework of your
 choice. Purchase of the
 print book includes a
 free eBook in PDF,
 Kindle, and ePub
 formats from Manning
 Publications. About the
 Technology The next
 step in the
 development of web-
 based software, single-
 page web applications
 deliver the sleekness
 and fluidity of a native

desktop application in a browser. If you're ready to make the leap from traditional web applications to SPAs, but don't know where to begin, this book will get you going. About the Book SPA Design and Architecture teaches you the design and development skills you need to create SPAs. You'll start with an introduction to the SPA model and see how it builds on the standard approach using linked pages. The author guides you through the practical issues of building an SPA, including an overview of MV* frameworks, unit testing, routing, layout management, data access, pub/sub, and client-side task automation. This book is full of easy-to-follow examples you can

apply to the library or framework of your choice. What's Inside Working with modular JavaScript Understanding MV* frameworks Layout management Client-side task automation Testing SPAs About the Reader This book assumes you are a web developer and know JavaScript basics. About the Author Emmitt Scott is a senior software engineer and architect with experience building large-scale, web-based applications. Table of Contents PART 1 THE BASICS What is a single-page application? The role of MV* frameworks Modular JavaScript PART 2 CORE CONCEPTS Navigating the single page View composition and layout Inter-module

interaction

Communicating with
the server Unit testing
Client-side task
automation

APPENDIXES Employee
directory example
walk-through Review of
the XMLHttpRequest
API Chapter 7 server-
side setup and
summary Installing
Node.js and Gulp.js

Interaction Design

John Wiley & Sons

**How the Grinch
Stole Christmas!
Read & Listen
Edition**

*Random
House Books for Young
Readers*

"Every Who down in
Who-ville liked
Christmas a lot . . . but
the Grinch, who lived
just north of Who-ville,
did NOT!" Not since
"Twas the night before
Christmas" has the
beginning of a

Christmas tale been so
instantly recognizable.
No holiday season is
complete without the
Grinch, Max, Cindy-
Lou, and all the
residents of Who-ville,
in this heartwarming
story about the effects
of the Christmas spirit
on even the smallest
and coldest of hearts.
Like mistletoe, candy
canes, and caroling,
the Grinch is a
mainstay of the
holidays, and his story
is the perfect gift for
young and old. This
Read & Listen edition
contains audio
narration.

**MIPS Assembly
Language
Programming**

Pearson

/*4204Q-9,
0-13-142044-5, Britton,
Robert, MIPS Assembly
Language
Programming, 1/E*/"

Users of this book will gain an understanding of the fundamental concepts of contemporary computer architecture, starting with a Reduced Instruction Set Computer (RISC). An understanding of computer architecture needs to begin with the basics of modern computer organization. The MIPS architecture embodies the fundamental design principles of all contemporary RISC architectures. This book provides an understanding of how the functional components of modern computers are put together and how a computer works at the machine-language level." Well-written and clearly organized, this book covers the basics of MIPS architecture,

including algorithm development, number systems, function calls, reentrant functions, memory-mapped I/O, exceptions and interrupts, and floating-point instructions." For employees in the field of systems, systems development, systems analysis, and systems maintenance.

The Cache Memory Book *Morgan Kaufmann*

The Second Edition of The Cache Memory Book introduces systems designers to the concepts behind cache design. The book teaches the basic cache concepts and more exotic techniques. It leads readers through some of the most intricate protocols used in complex

multiprocessor caches. Written in an accessible, informal style, this text demystifies cache memory design by translating cache concepts and jargon into practical methodologies and real-life examples. It also provides adequate detail to serve as a reference book for ongoing work in cache memory design. The Second Edition includes an updated and expanded glossary of cache memory terms and buzzwords. The book provides new real world applications of cache memory design and a new chapter on cache "tricks". Illustrates detailed example designs of caches Provides numerous examples in the form of block

diagrams, timing waveforms, state tables, and code traces Defines and discusses more than 240 cache specific buzzwords, comparing in detail the relative merits of different design methodologies Includes an extensive glossary, complete with clear definitions, synonyms, and references to the appropriate text discussions

Computer Networks

Appropriate for Computer Networking or Introduction to Networking courses at both the undergraduate and graduate level in Computer Science, Electrical Engineering, CIS, MIS, and Business Departments. Tanenbaum takes a structured approach to

explaining how networks work from the inside out. He starts with an explanation of the physical layer of networking, computer hardware and transmission systems; then works his way up to network applications.

Tanenbaum's in-depth application coverage includes email; the domain name system; the World Wide Web (both client- and server-side); and multimedia (including voice over IP, Internet radio video on demand, video conferencing, and streaming media.

Logic and Computer Design Fundamentals

Prentice Hall

For courses in Logic and Computer design.

Understanding Logic and Computer Design for All Audiences Logic and Computer Design Fundamentals is a thoroughly up-to-date text that makes logic design, digital system design, and computer design available to readers of all levels. The Fifth Edition brings this widely recognized source to modern standards by ensuring that all information is relevant and contemporary. The material focuses on industry trends and successfully bridges the gap between the much higher levels of abstraction people in the field must work with today than in the past. Broadly covering logic and computer design, Logic and Computer Design Fundamentals is a flexibly organized

source material that allows instructors to tailor its use to a wide range of audiences.

Recovering Histories

Life and Labor after Heroin in Reform-Era China *University of California Press*

Heroin first reached Gejiu, a Chinese city in southern Yunnan known as Tin Capital, in the 1980s. Widespread use of the drug, which for a short period became “easier to buy than vegetables,” coincided with radical changes in the local economy caused by the marketization of the mining industry. More than two decades later, both the heroin epidemic and the mining boom are often discussed as recent history. Middle-aged

long-term heroin users, however, complain that they feel stuck in an earlier moment of the country’s rapid reforms, navigating a world that no longer resembles either the tightly knit Maoist work units of their childhood or the disorienting but opportunity-filled chaos of their early careers. Overcoming addiction in Gejiu has become inseparable from broader attempts to reimagine laboring lives in a rapidly shifting social world. Drawing on more than eighteen months of fieldwork, Nicholas Bartlett explores how individuals’ varying experiences of recovery highlight shared challenges of inhabiting China’s contested present.

The Coding Manual

**for Qualitative
Researchers** *SAGE*

The Second Edition of Johnny Saldaña's international bestseller provides an in-depth guide to the multiple approaches available for coding qualitative data. Fully up to date, it includes new chapters, more coding techniques and an additional glossary. Clear, practical and authoritative, the book: -describes how coding initiates qualitative data analysis -demonstrates the writing of analytic memos -discusses available analytic software -suggests how best to use The Coding Manual for Qualitative Researchers for particular studies. In total, 32 coding methods are profiled that can be applied to a range of research

genres from grounded theory to phenomenology to narrative inquiry. For each approach, Saldaña discusses the method's origins, a description of the method, practical applications, and a clearly illustrated example with analytic follow-up. A unique and invaluable reference for students, teachers, and practitioners of qualitative inquiry, this book is essential reading across the social sciences.

**The State of the
World's Land and
Water Resources for
Food and
Agriculture**

**Managing Systems
at Risk** *Routledge*

The State of the World's Land and Water Resources for

Food and Agriculture is FAO's first flagship publication on the global status of land and water resources. It is an 'advocacy' report, to be published every three to five years, and targeted at senior level decision makers in agriculture as well as in other sectors. SOLAW is aimed at sensitizing its target audience on the status of land resources at global and regional levels and FAO's viewpoint on appropriate recommendations for policy formulation. SOLAW focuses on these key dimensions of analysis: (i) quantity, quality of land and water resources, (ii) the rate of use and sustainable management of these resources in the context of relevant socio-economic driving factors and concerns, including food security and poverty, and climate change. This is the first time that a global, baseline status report on land and water resources has been made. It is based on several global spatial databases (e.g. land suitability for agriculture, land use and management, land and water degradation and depletion) for which FAO is the world-recognized data source. Topical and emerging issues on land and water are dealt with in an integrated rather than sectoral manner. The implications of the status and trends are used to advocate remedial interventions which are tailored to major farming systems within different

geographic regions.

Computer Systems

A Programmer's Perspective

For Computer Systems, Computer Organization and Architecture courses in CS, EE, and ECE departments. Few students studying computer science or computer engineering will ever have the opportunity to build a computer system. On the other hand, most students will be required to use and program computers on a near daily basis. *Computer Systems: A Programmer's Perspective* introduces the important and enduring concepts that underlie computer systems by showing how these ideas affect the correctness, performance, and

utility of application programs. The text's hands-on approach (including a comprehensive set of labs) helps students understand the under-the-hood operation of a modern computer system and prepares them for future courses in systems topics such as compilers, computer architecture, operating systems, and networking.

A Guide to the Project Management Body of Knowledge (PMBOK® Guide) - Seventh Edition and The Standard for Project Management (RUSSIAN) *Project Management Institute* PMBOK® Guide is the go-to resource for project management practitioners. The project management profession has

significantly evolved due to emerging technology, new approaches and rapid market changes. Reflecting this evolution, The Standard for Project Management enumerates 12 principles of project management and the PMBOK® Guide &– Seventh Edition is structured around eight project performance domains. This edition is designed to address practitioners' current and future needs and to help them be more proactive, innovative and nimble in enabling desired project outcomes. This edition

of the PMBOK® Guide: • Reflects the full range of development approaches (predictive, adaptive, hybrid, etc.); • Provides an entire section devoted to tailoring the development approach and processes; • Includes an expanded list of models, methods, and artifacts; • Focuses on not just delivering project outputs but also enabling outcomes; and • Integrates with PMI standards+™ for information and standards application content based on project type, development approach, and industry sector.