
How The Penguins Saved Veronica

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PSIM supports TI's InstaSPIN-FOC sensorless motor control algorithm in simulation and SimCoder auto code generation. With this capability, PSIM provides the easiest way for users to evaluate the performance of a motor control algorithm with InstaSPIN. The core of the InstaSPIN algorithm is a FAST estimator that performs parameter identifications and calculates flux, angle, speed, and torque based on motor phase voltages and currents and dc bus voltage.

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Simulation and Code Generation of TI InstaSPIN Using DRV8305 EVM 3 1. Quick Start If you wish to run the examples described in this tutorial right away, follow the steps below. To run the simulation with InstaSPIN control: - Connect the computer to the F28069M LaunchPad. The LaunchPad can be by itself or connected to the DRV8305 EVM board.

Simulation and Code Gen of TI InstaSPIN (DRV8305)

You can also configure the MPC block to use single-precision data in simulation and code generation. mdl2 = 'mpc_rtwdemo_single' ; open_system (mdl2) To do so, set the Output data type property of the MPC Controller block to single. Simulate the model in Simulink.

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The carrier waveform is a triangular waveform from - 0.5 to 0.5, with the phase delay set to 180 deg. and a switching frequency of 20kHz.

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Simulation and Code Generation with TI InstaSPIN Block ...

and for auto code generation Simulation and Code Gen of TI InstaSPIN (DRV8305) simulation or in a SimCoder circuit for auto code generation for DSP hardware At the moment, only F2806x DSP is supported F2802x DSP can be supported upon request This tutorial describes how the PIL InstaSPIN block is used for simulation and for auto code generation ...

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- Select Simulate >> Run Simulation to run the simulation. After simulation is completed, display waveforms in SIMVIEW. To generate code from the schematic with InstaSPIN control, and run code on the hardware to spin the motor: - Insert the F28069M controlCARD to the DIM100 connector of the DRV8312 EVM board.

Simulation and Code Gen of TI InstaSPIN (DRV8312)

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Generating and reviewing code can be as simple as preparing the model for code generation with the Quick Start tool. Then, using code tools accessible from the Simulink Editor, you can configure data interfaces, initiate code generation, and verify the generated code. This tutorial uses example model rtwdemo_secondOrderSystem.

Generate C Code for a Model - MATLAB & Simulink

Block (InstaSPIN), refer to the tutorial “Tutorial – Simulation and code generation of TI InstaSPIN using DRV8305 EVM.pdf”. This tutorial describes, in step by step, how to set up and perform PIL simulation using the general PIL block. The process involves the following steps: 1. Preparing the code for PIL simulation 2. Setting up in PSIM 3.

TUTORIAL Processor-In-the-Loop Simulation

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The Simulation generates code based on the StateMachine model. That model can have triggers. These triggers can be set to be from external devices like PLCs, but you do need to alter

the code generated to set your interface to them. See the section Leveraging existing code, in the Help topic Code Generation for Executable StateMachines.

Executable State Machine Simulation and Code Generation ...

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